

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,057,981 B2
APPLICATION NO. : 10/766015
DATED : June 6, 2006
INVENTOR(S) : A. Kano et al.

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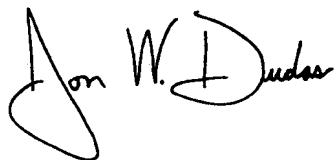
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, lines 55, through column 9, line 4, please amend the paragraph as follows:

Each converter 901 is a circuit for converting a fiber channel protocol and a serial ATA protocol into each other in the same manner as the converter 801 in Fig. 8. The converter 901 is connected to a PBC 602 of an FC-AL 506. A plurality of hard disk drives 51 are connected to each converter 901 through switches 902 respectively. The switches 902 are circuits for selecting one ~~hard disk drive 51~~ ~~converter 901~~ with which communication should be established when the hard disk drives 51 are connected to a plurality of converters 901. Each switch 902 is provided in each disk drive unit 52. Each converter 901 is formed out of one chip or a plurality of circuits in which a protocol converting function is incorporated. For example, the converter 901 can be implemented by the configuration of an SATA master device disclosed in "US Patent Application Publication No. 2003/0135577". The converter 901 is mounted on the controller board 59, the power supply controller board 56 or the like.

Signed and Sealed this

Eleventh Day of September, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office